

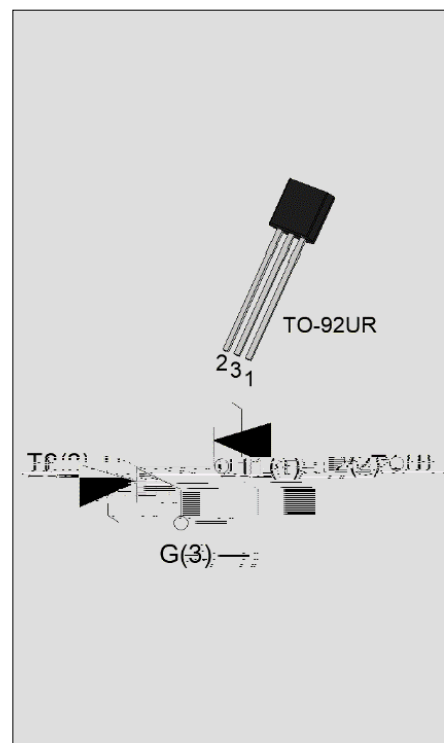


DESCRIPTION:

The ACJT210-6UR triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. The ACJT210-6UR embeds a TVS structure to absorb the inductive turn-off energy such as those described in the IEC 61000-4-5 standards. Package TO-92UR is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	2	A
V_{DRM}/V_{RRM}	600	V
$I_{GT} / /$	10/10/10	mA



ABSOLUTE MAXIMUM RATINGS

Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	600	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	600	V
RMS on-state current	$I_{T(RMS)}$	2	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	25	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		27.5	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	3.125	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$
Peak gate current ($t_p=20\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	2	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.1	W
Peak gate power	P_{GM}	10	W



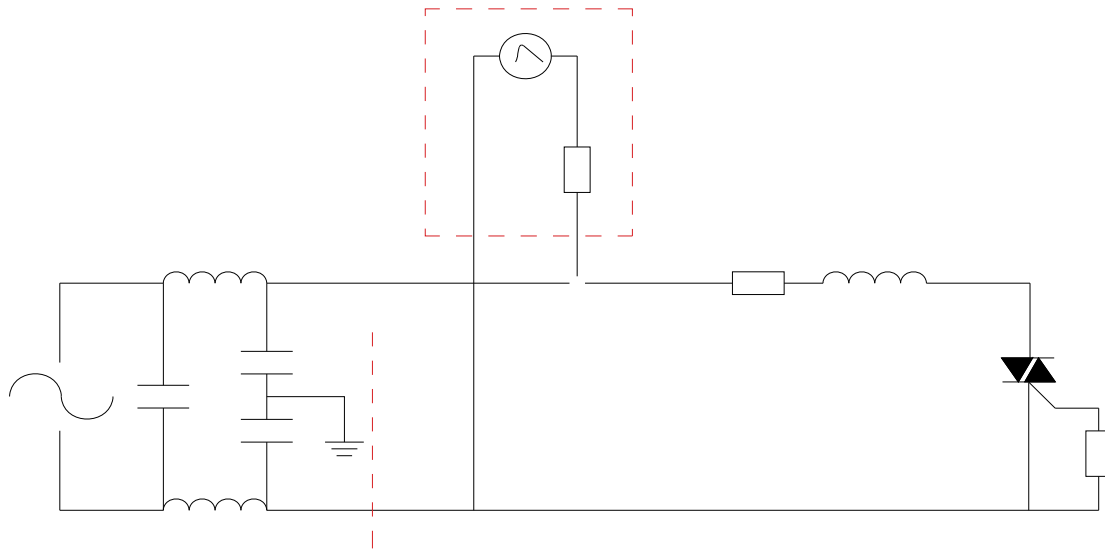
Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4.5	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

I_{GT}	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k\Omega$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	25	mA
				35	
I_H	$I_T=100mA$		MAX.	15	mA
dV/dt	$V_D=400V$ Gate Open $T_j=125$		MIN.	1000	V/ μs
$(dI/dt)_C$	$(dV/dt)_C=10V/\mu s$, $T_j=125$		MIN.	3	A/ms
t_{on}	$I_G=20mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	2.5	μs
t_{off}				25	
V_{CL}	$I_{CL}=0.1mA$ $t_p=1ms$		MIN.	700	V



FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





ORDERING INFORMATION





DELIVERY MODE

TO-92UR	Bulk Pack	1,000	10,000	50,000

P12 0		5	
P2	5.95	6.35	6.75
P	-1.00	0	1.00
F1 F2	2.30	2.50	2.70
F1-F2	-0.10	0	0.10
W	17.50	18.00	19.00
W0	5.50	6.00	6.50
W1	8.50	9.00	9.50
W2			1.00
D0	3.80	4.00	4.20
H	-1.00	0	1.00
L1	2.50		
H	18.00	19.00	20.00

