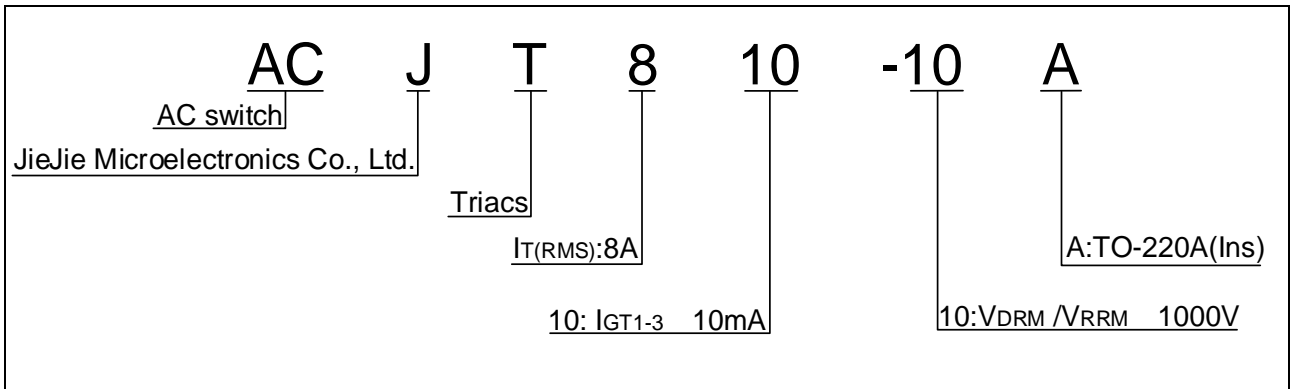


ACJT810-10A

Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	2	kV

ORDERING INFORMATION



MARKING

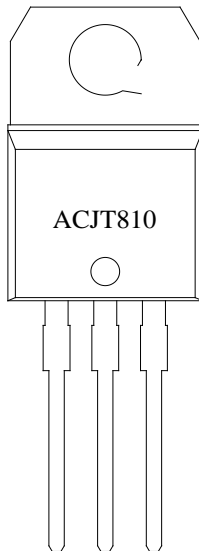


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ACJT810-10A

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents.

Jiangsu JieJie assumes no responsibility for any infringement of other rights of third

parties which may result from the use of the information provided in this document.