

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{PP}	3.5	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	10	mA
				25	
V_{GT}		ALL	MAX.	1.3	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	15	mA
				30	

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JST131U-600E

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FIG.1: Maximum power dissipation versus on-state current

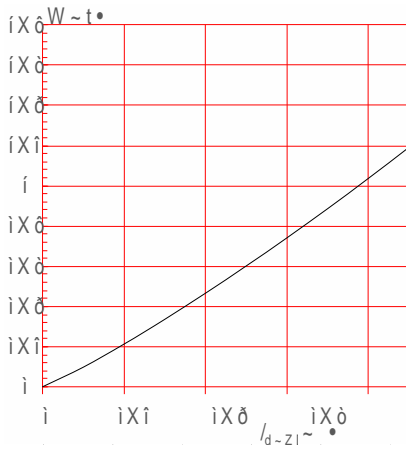


FIG.2: On-state current versus case temperature

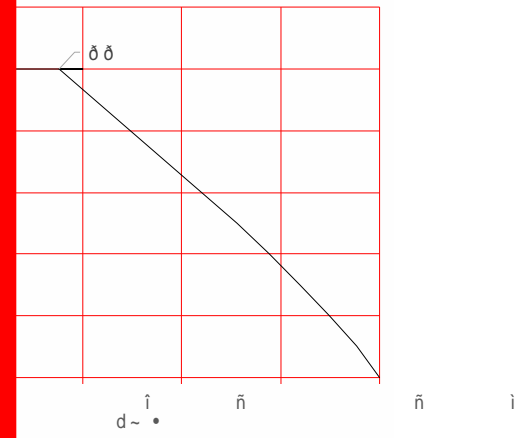


FIG.3: Surge peak on-state current versus number of cycles

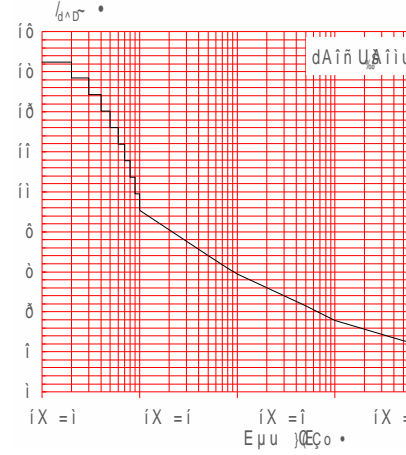
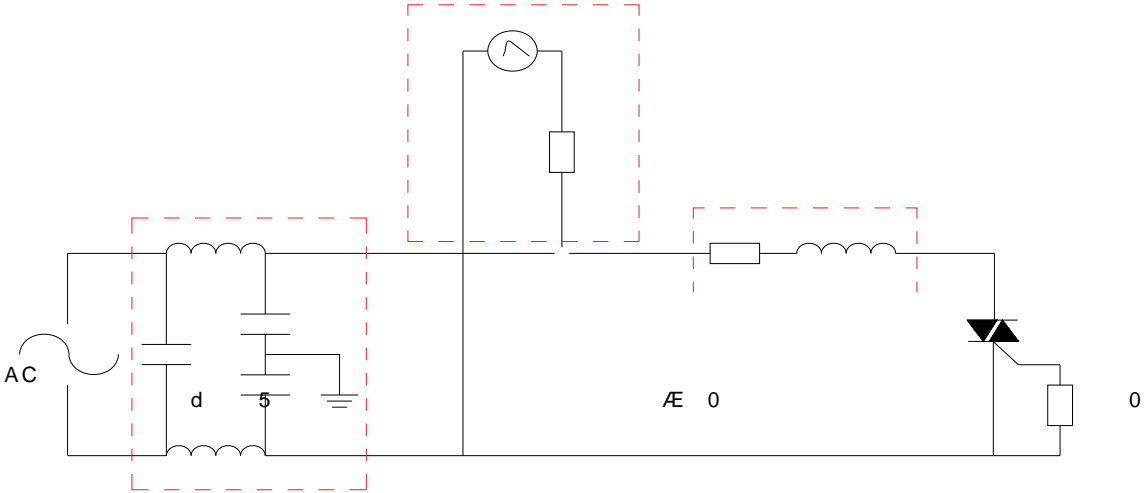


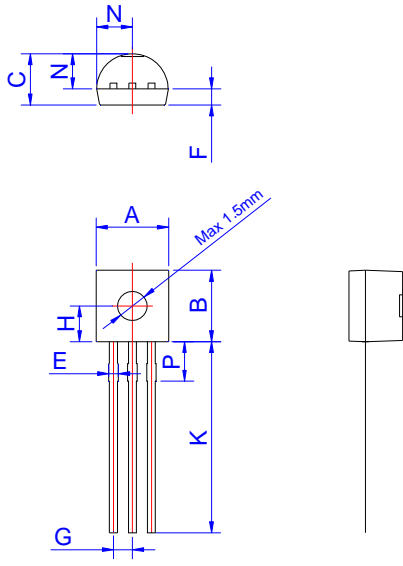
FIG.4: On-state characteristics



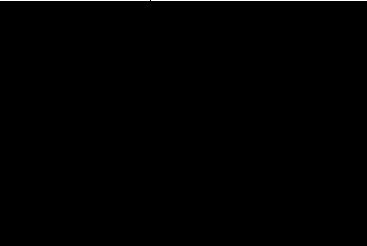
FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



PACKAGE MECHANICAL DATA



[Redacted]		[Redacted]		[Redacted]
[Redacted]	[Redacted]	[Redacted]	[Redacted]	[Redacted]



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