

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	5	mA
				10	
V _{GT}		ALL	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3k	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	-	MAX.	15	mA
		-		25	
I _{SM}	I _G =100mA		MAX.	15	mA
(dv/dt) _c	V _D =540V Gate Open T _j =110		MIN.	100	V/μs
(di/dt) _c	(di/dt) _c =1.8A/ms, T _j =110		MIN.	2.5	V/μs
t _{on}	I _G =20mA I _A =200mA I _R =20mA T _j =25		TYP.	1	μs
t _{off}				12	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V _{TM}	I _{TM} =5A t _p =380μs	T _j =25	1.7	V
V _{TO}	Threshold voltage	T _j =125	0.94	V
R _D	Dynamic resistance	T _j =125	124	m
I _{DRM}	V _D =V _{DRM} V _R =V _{RDM}	T _j =25	5	μA
I _{RDM}		T _j =125	0.4	m

ORDERING INFORMATION

J ST 136 H -800 D T

FIG.1: Maximum power dissipation versus RMS on-state current

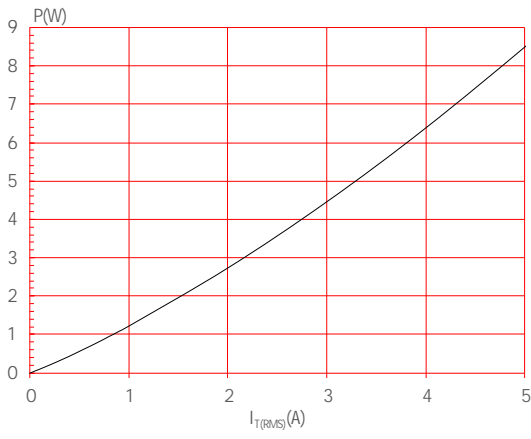


FIG.2: RMS on-state current versus case temperature

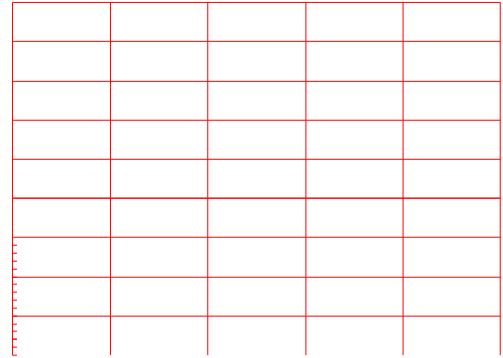
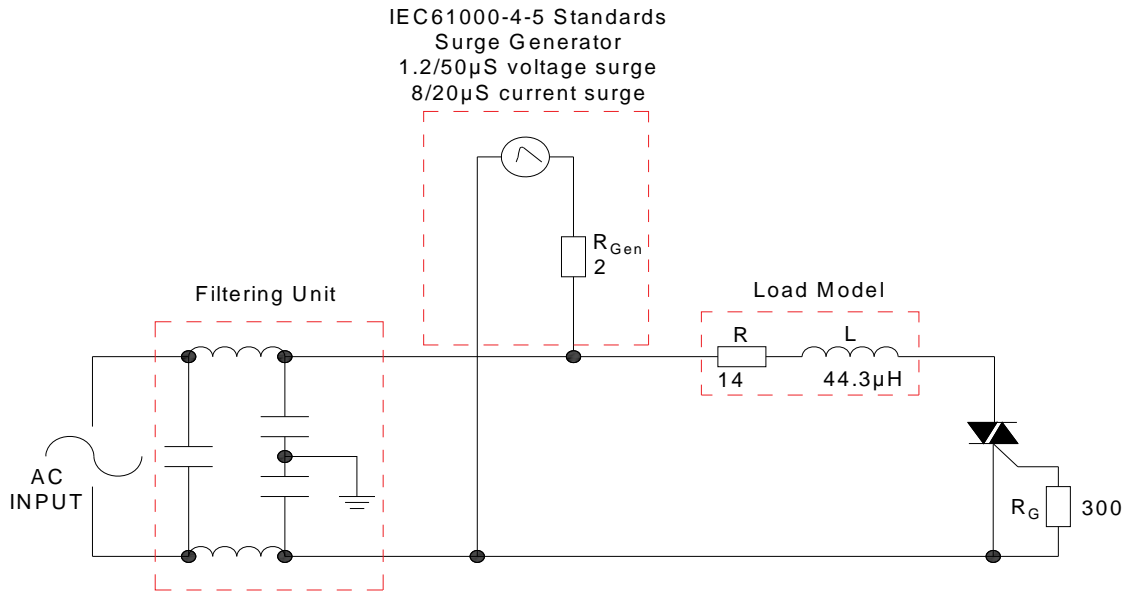


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



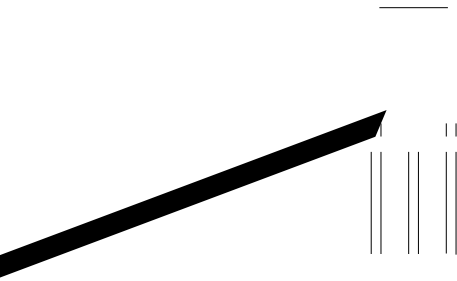
LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

ORDERING INFORMATION


Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST136H-800D	800	5	10	TO-251	80	Tu

Date	Revision	Changes



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.
Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.