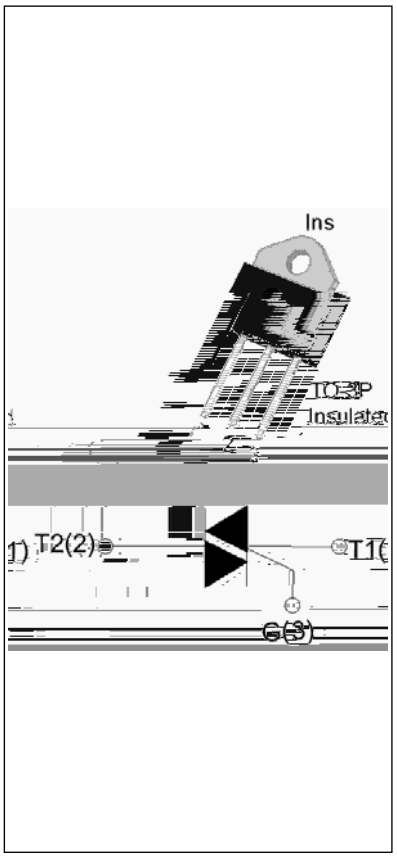
JST30Z-1200BW 30A TRIAC

Rev.A.1.1

The JST30Z-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST30Z-1200BW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST30Z-1200BW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-3P is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	30	A
V_{DRM} /V_{RRM}	1200	V
I_{GT} / /	50/50/50	mA

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
Repetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V
RMS on-state current ($T_C \leq$ 79)	$I_{T(RMS)}$	30	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25)	I_{TSM}	300	A
Non repetitive surge peak on-state current (full cycle , t_p=16.6ms , T_j=25)		330	
I^2t value for fusing (t_p=10ms , T_j=25)	I^2t	450	A^2s
Critical rate of rise of on-state current (I_G=2 × I_{GT} , f=100Hz , T_j=125)	dI/dt	100	A/μs
Peak gate current (t_p=20μs , T_j=125)	I_{GM}	4	A

Average gate power dissipation (T_j=125)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	2.5	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1.3	V
V_{GD}	V_D =V_{DRM} T_j=125 R_L =3.3kΩ	- -	MIN.	0.15	V
I_L	I_G=1.2I_{GT}	-	MAX.	90	mA
				100	
I_H	I_T =500mA		MAX.	80	mA
dV/dt	V_D=800V Gate Open T_j=125		MIN.	1200	V/μs
(dI/dt)c	(dV/dt)c=20V/μs T_j=125		MIN.	25	A/ms
t_{on}	I_G=80mA I_A=400mA I_R=40mA T_j=25		TYP.	10	μs
t_{off}				70	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =42A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.73	V
R_D	Dynamic resistance	T_j=125	25	mΩ
I_{DRM}	V_D=$V_{DRM}$$V_R$ =V_{RRM}	T_j=25	10	μA
I_{RRM}		j=125	4	mA

J ST 30 Z -1200 BW

- J: JieJie Microelectronics Co., Ltd.
- ST: Triacs
- 30: $I_{T(RMS)}$:30A
- Z: Z:TO-3P(Ins)
- -1200: 1200:V_{DRM} /V_{RRM} ≥ 1200V
- BW: BW:I_{GT1-3} ≤ 50mA

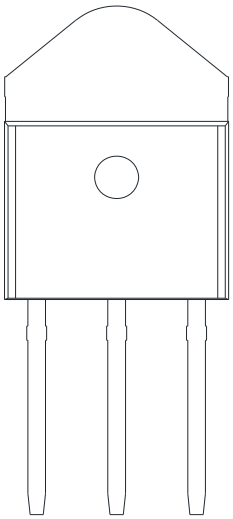


FIG.1: Maximum power dissipation versus RMS on-state current

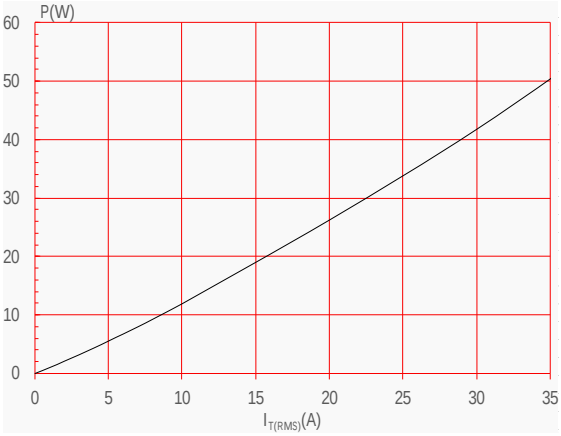


FIG.2: RMS on-state current versus case temperature

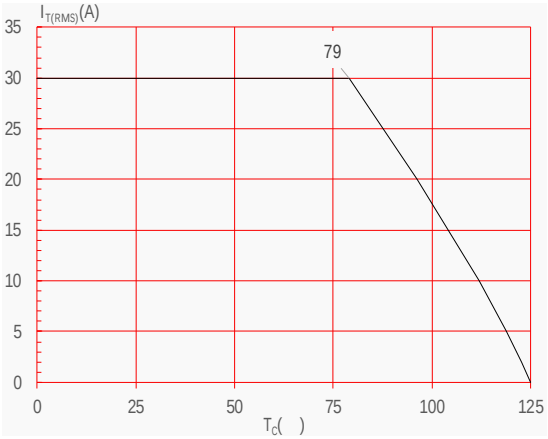


FIG.3: Surge peak on-state current versus number of cycles

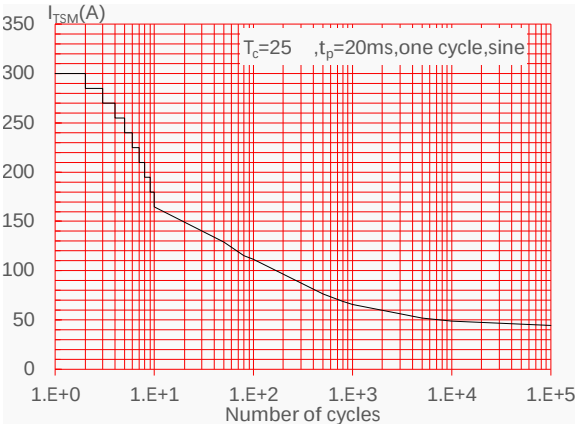


FIG.4: On-state characteristics

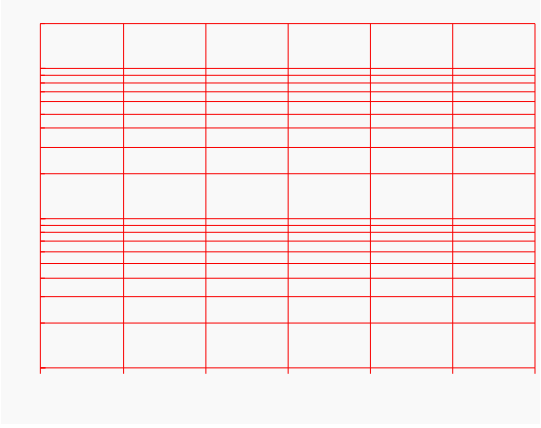


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards